

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,815,811 B2
DATED : November 9, 2004
INVENTOR(S) : Hiroyuki Ozawa et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 17, line 54, to Column 18, line 3,
Please delete "Claims 1, 2 and 3."

Column 18,

Line 16, please insert the words after "layer" as following:

-- and thereby achieving constant wire density, reduced parasitic capacitance and reduced signal delay --

Line 32, please insert the words after "width" as following:

-- and thereby achieving constant wire density, reduced parasitic capacitance and reduced signal delay --

Column 18, from line 33 to line 36,

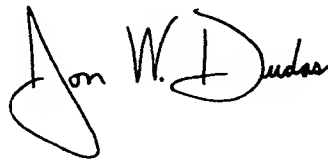
Please delete "Claim 6"

Line 48, please insert the words after "distance" as following:

-- and thereby achieving constant wire density, reduced parasitic capacitance and reduced signal delay --

Signed and Sealed this

Twenty-eighth Day of June, 2005



JON W. DUDAS
Director of the United States Patent and Trademark Office